

FIG. 1 PRIOR ART

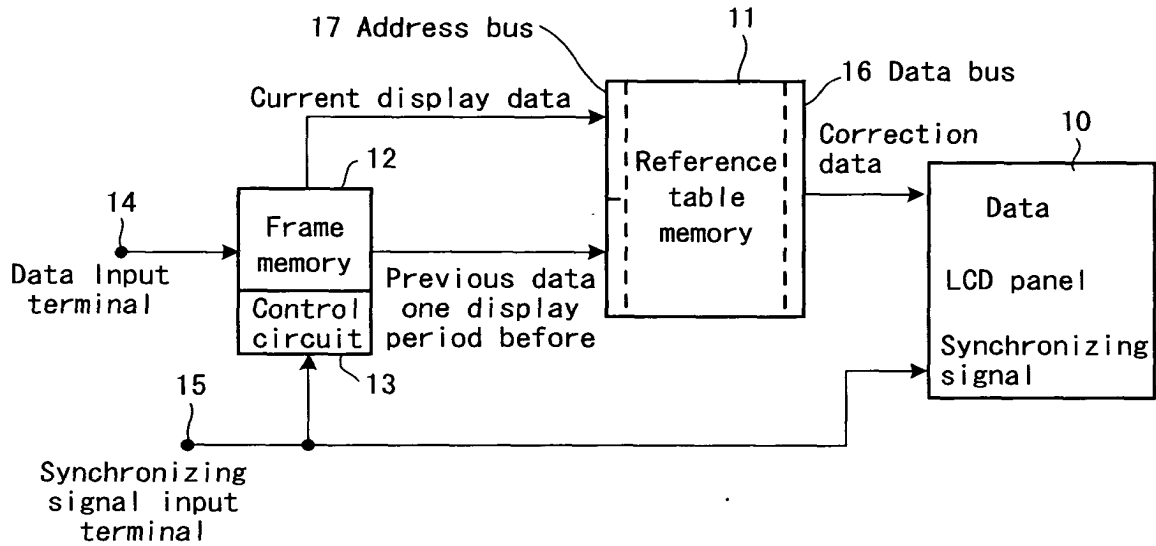


FIG. 2 PRIOR ART

		Address (current image data : 8bit)																	
		0	1	2	3	4	5						250	251	252	253	254	255	
Address (previous image data : 8bit)	0	0	2	4	6	8	9						252	253	254	255	255	255	
	1	0	1	2	5	7								253	264	255	255	255	
	2	0	0	2	4	6								253	254	255	255	255	
	3	0	1	1	3	4								253	254	255	255	255	
	4	0	2	3	3										254	255	255	255	
	5	0																255	
	250	0																	255
	251	0	0	0	1										251	253	255	255	255
	252	0	0	0	1	2									250	252	255	255	255
	253	0	0	0	1	2									250	251	253	255	255
	254	0	0	0	1	2									249	250	252	254	255
255	0	0	0	1	2	3							247	249	250	252	254	255	

FIG. 3 PRIOR ART

		Previous frame data							
Current frame data	0	32	64	96	128	160	192	224	255
	0								
	32								
	64								
	96								
	128								
	160								
	192								
	224								
	255								

FIG. 4

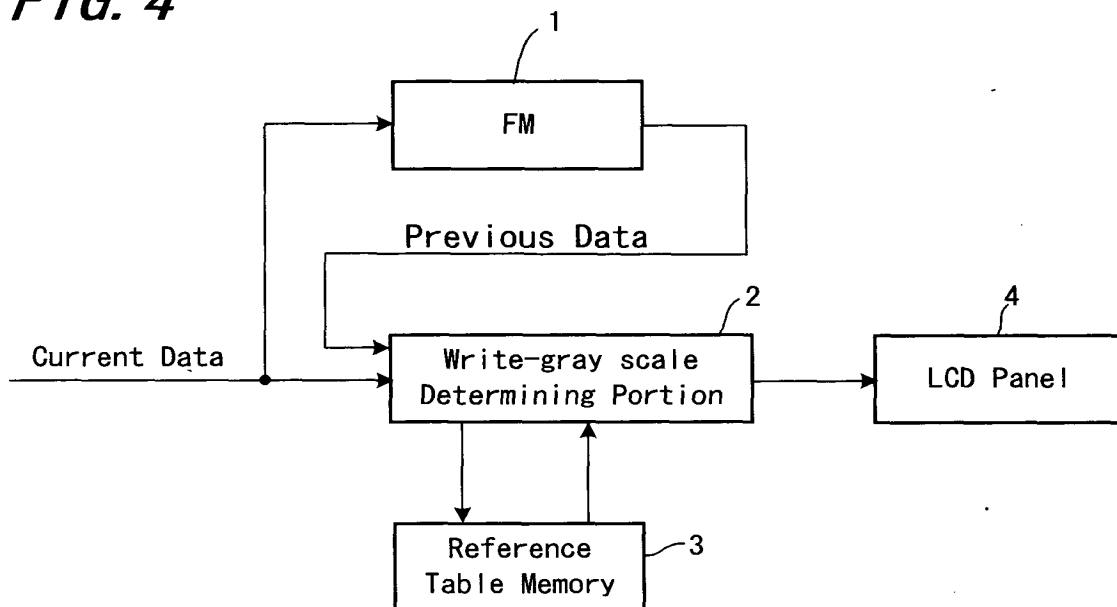


FIG. 5

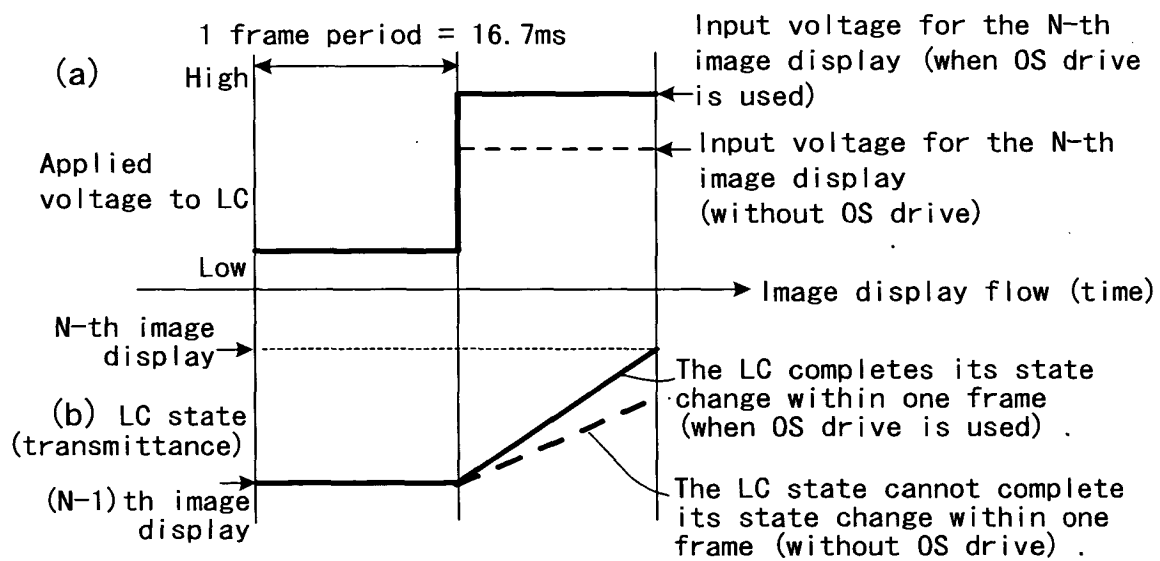


FIG. 6

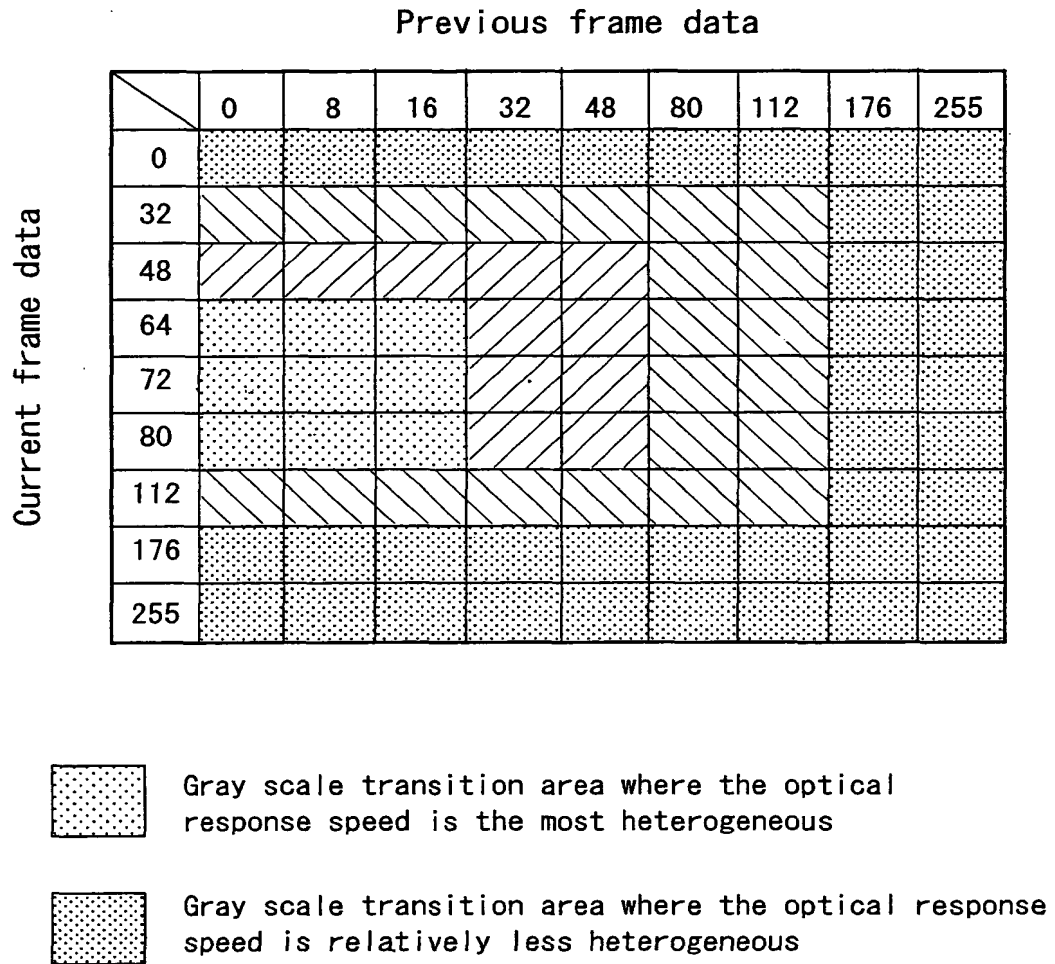


FIG. 7A

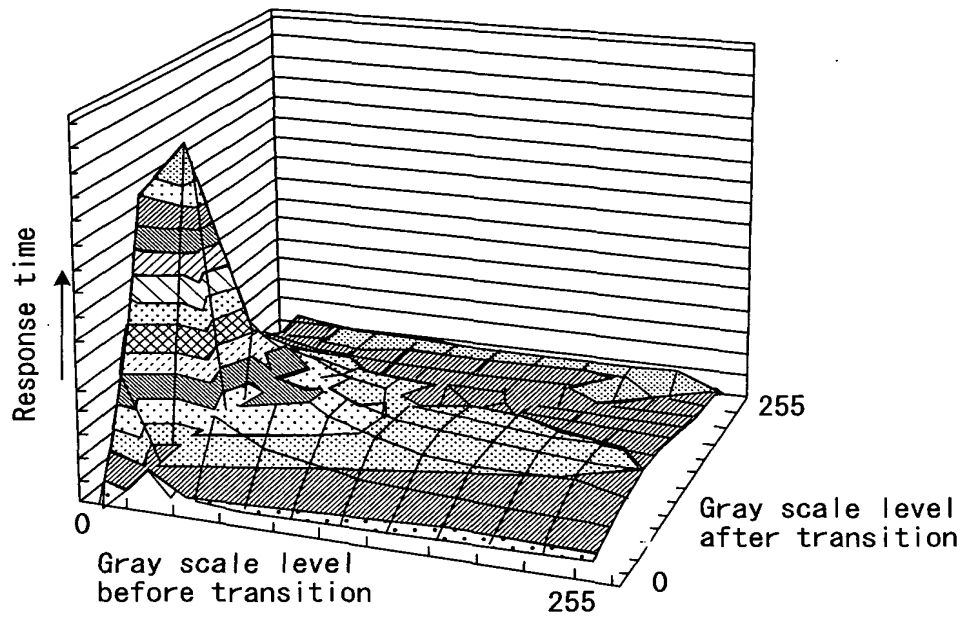


FIG. 7B

